

Option  
4HP F/P

ETH  
1

ETH  
2

ETH  
3

ETH  
4

ETH  
5

Front  
Panel  
I/O

10BASE-T  
100BASE-TX RMI  
PHY  
TLK110

10BASE-T  
100BASE-TX RMI  
PHY  
TLK110

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100BASE-TX RMI  
PHY  
TLK110

10BASE-T  
100BASE-TX RMI  
PHY  
TLK110

10BASE-T  
100BASE-TX RMI  
PHY  
TLK110

SC2

SC1

Mezzanine Expansion to C80-EXP

DDR3L  
512Mx8

EEPROM

EERAM  
2k x 8

EPCQ  
Flash

I2C

SDRAM

MAC/RMII

MAC/RMII

5

5

5CGXFC7

Cyclone® V

JTAG

SD1

PCIe

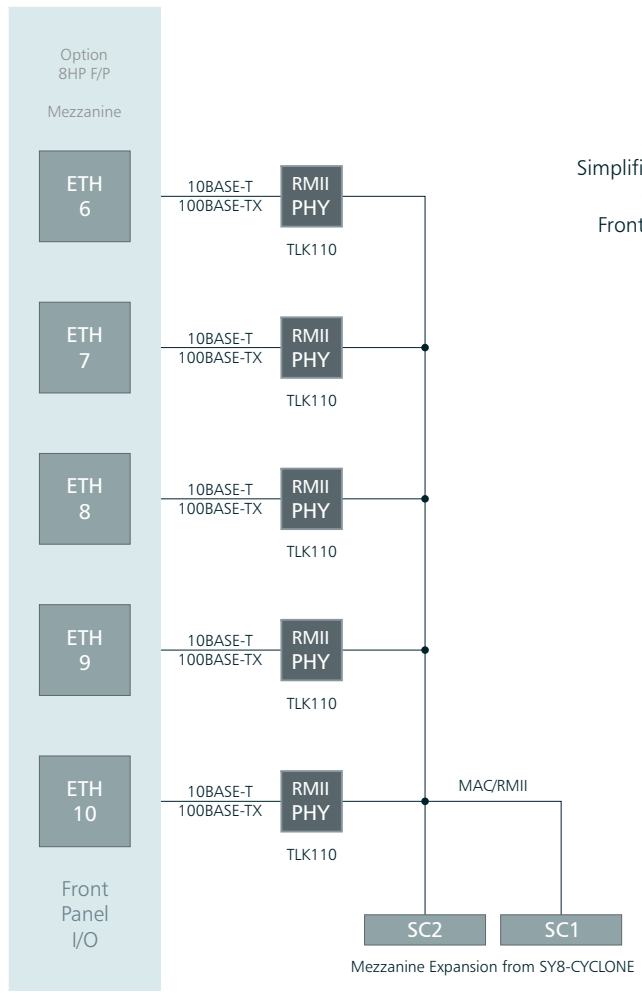
4

P6

Backplane  
CompactPCI  
Serial

Peripheral  
Slot

P1



Simplified Block Diagram  
C80-EXP  
Front I/O Mezzanine

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Simplified Block Diagram  
C81-SI  
FPGA Mezzanine

